

PATENT
P56181

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JU-HEON LEE

Serial No.: 09/685,138

Examiner: HUYNH, KIM NGOC

Filed: 11 October 2000

Art Unit: 2182

For: PORTABLE INTEGRATED CIRCUIT MEMORY DEVICE FOR USE WITH
UNIVERSAL BUS

INFORMATION DISCLOSURE STATEMENT

RECEIVED

Mail Stop : Non-Fee Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SEP 08 2003

Technology Center 2100

Sir:

Pursuant to 37 C.F.R. §§ 1.56, and 1.97 and 1.98 applicant cites, lists, and briefly discusses and encloses copies of the following art references cited in a *Notice To Submit Response* from the Korean Industrial Property Office in corresponding Korean priority application No.2003-0032798 which is a divisional application of the Korean patent application No. 1999-43872. ::

U.S. PATENT REFERENCE:

<u>Patent No.</u>	<u>Inventor</u>	<u>Issued Date</u>
• 5,844,986	Davis	1 December 1998
• 6,012,103	Sartore <i>et al.</i>	4 January 2000

FOREIGN PATENT REFERENCE:

<u>Patent No.</u>	<u>Inventor</u>	<u>Issued Date</u>
• WO 99/01820	Sartore <i>et al.</i>	14 January 1999

DISCUSSION

Davis U.S. '986 describes a system to securely update an executable code with providing a procedure to authenticate and validate a code update, such as a BIOS upgrade for example, using cryptographic technology.

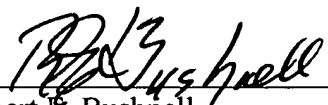
Sartore *et al.* WO '820 pertains to a bus interface system.

Sartore *et al.* U.S. '103 relates to a system and method for connecting a computer system to a computer bus.

Pursuant to 37 CFR § 1.97 (e)(1), that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information designated in section 1.56(e) more than three months prior to the filing of the information disclosure statement.

No fee is incurred by this Statement.

Respectfully submitted,


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